**LOGIC BASED PATTERN DISCOVERY USING COHERENT RULES**

PROJECT ‘B’ REPORT

Submitted in partial fulfillment of the

Requirements for degree of

BACHELOR OF ENGINEERING

In

COMPUTER ENGINEERING

By

Seat no. Group no. 42

Sayali Sakhalkar

Sonal Chaplot

Apurva Kulkarni

Under the guidance of

Ms. Arti Deshpande

(Assistant Professor, TSEC)



Department of Computer Engineering

Thadomal Shahani Engineering College

University of Mumbai

2012-2013

Project Entitled**: LOGIC BASED PATTERN DISCOVERY lllllllllllllllllllllllll lUSING COHERENT RULES**

### Submitted by: Sayali Sakhalkar

### Sonal Chaplot

### Apurva Kulkarni

in partial fulfillment of degree of B.E. in Computer Engineering is approved

Guide Examiners

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Mrs. Arti Deshpande

(Assistant Professor,

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Mr Jayant Gadge

Head of Department Principal

Date: \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

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### Sayali Sakhalkar

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